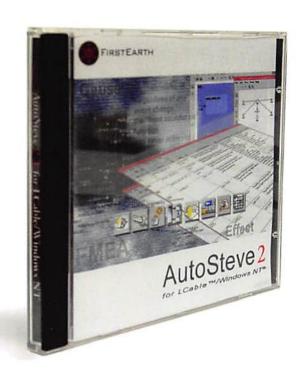
0 to FMEA in under 6 seconds

You want to reduce the time spent on electrical design FMEA, without compromising on quality. You want real feedback that schematics have been drawn correctly. You would like to perform Sneak Circuit Analysis. You are short of engineer effort in your department.



AutoSteve 2

the **fastest**, **easiest** way to perform electrical FMEA

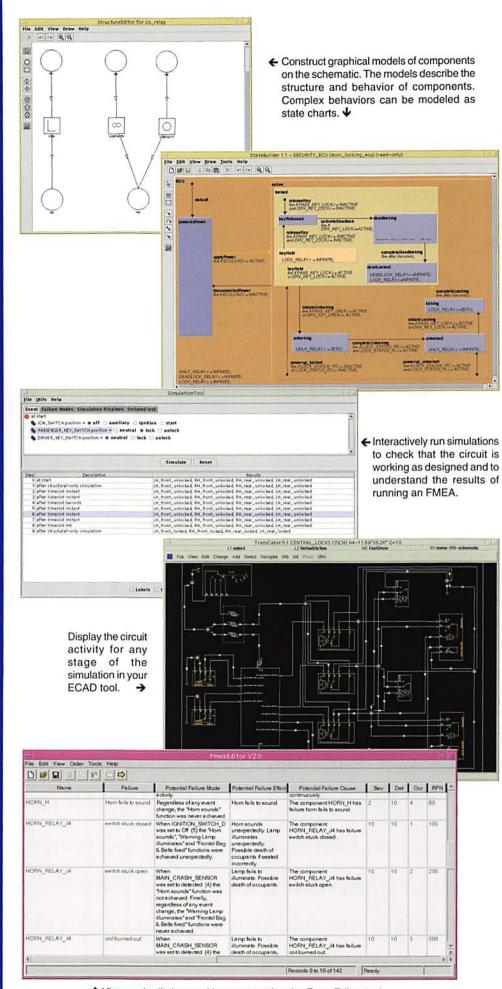
AutoSteve, unlike other FMEA software, simulates the behavior of electrical circuits and automatically produces an FMEA report that describes the effect of failures on the circuit's functionality. It provides visual feedback of circuit operation to verify correct behavior and to illustrate faulty behavior. It detects sneak paths with high accuracy and minimal effort. It automates a lot of what Steve* does, and he wouldn't be without it.

^{*} Early versions of AutoSteve were developed with the assistance of two engineers, both from leading automotive manufacturers, who both happened to be called Steve.

Key_{Features}

- Supports engineers by automating key stages of FMEA production.
- FMEA results are presented in an easy to read textual form, similar to those that an engineer would produce.
- · Analyses a schematic in minutes.
- Information entered into AutoSteve can be reused with other schematics, therefore reducing the amount of information to be specified for new schematics.
- Models complex component behavior, e.g. ECUs, using state machines.
- Close integration with your ECAD tool.
- AutoSteve uses qualitative simulation to perform FMEA; therefore, models are easy to build and don't require the detail necessary for numerical models.
- View results of a particular failure graphically through coloring of the schematic and component state machines.
- FMEA report can be exported to other FMEA result handling software, e.g. FMEAplus.
- Find Sneak Circuits with the SCA tool.
- Perform single and multiple failure FMEA.
- AutoSteve is supplied with a library of common component models.
- Use AutoSteve's simulation tools to perform Virtual Yellowboarding

 proving that the schematic works as designed, long before a physical prototype becomes available.
- · QS9000 compliant results.



↑ View and edit the resulting report using the FmeaEditor tool.

